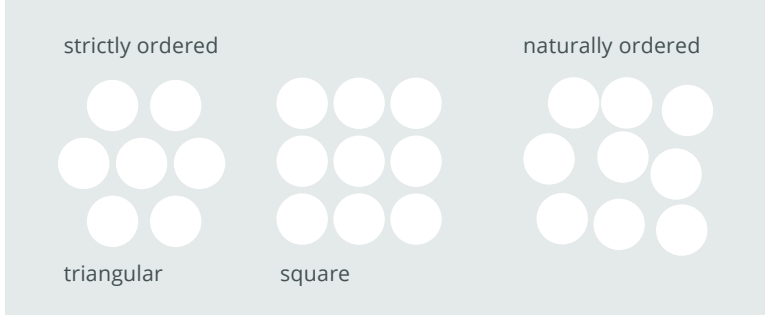


# MakroPor

## Macroporous Silicon FactSheet

### PORE GEOMETRIES

The planar pore geometry can be predefined so that periodic arrangements as well as more complex structures including trenches and cavities can be formed. Without prestructuring, the pore arrangement is driven by self organization. Due to the fixed pore distance, the size and distance distribution is more precise for the predefined structures.

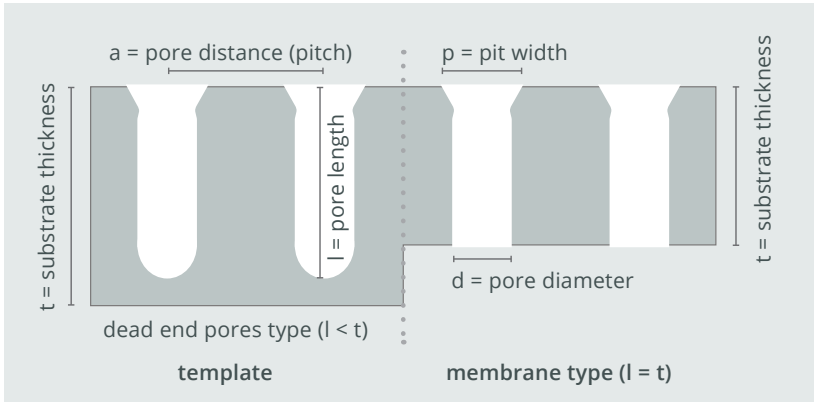


### STANDARD PARAMETERS

Our standard lithographies utilize  $p = 0.5 a$ .

A funnel shaped pore entrance followed by a small bottleneck (length in the range of the pore diameter) will be formed on top of the pore structures.

Two types of structures are available: „dead end type“ (template) pores inside bulk silicon substrate and „membrane type“.

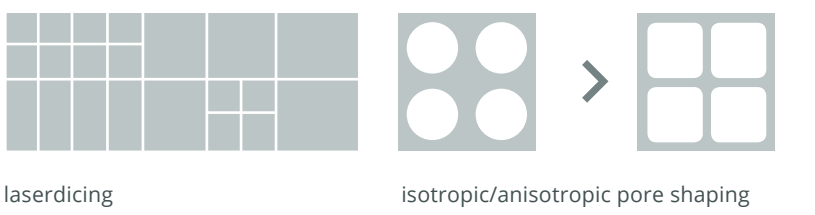


### TECHNICAL DATA

Pore geometry	trigonal, square, custom defined lithography including trenches, rims, cavities etc.
Pore distance ( <b>a</b> )	standard stock material: $a = 1.5 \mu\text{m}$ , $4.2 \mu\text{m}$ trigonal lattice, $a = 12 \mu\text{m}$ , $20 \mu\text{m}$ square lattice
Pore diameter ( <b>d</b> )	$0.8 - 18 \mu\text{m}$
Porosity ( <b>p</b> )	$20 - 60 \%$
Pore length ( <b>l</b> )	$1 - 500 \mu\text{m}$ (depending on substrate thickness)
Chips size	up to 6" diameter, custom sizes/shapes via laserdicing, small scale custom process development possible

### POSTPROCESSING

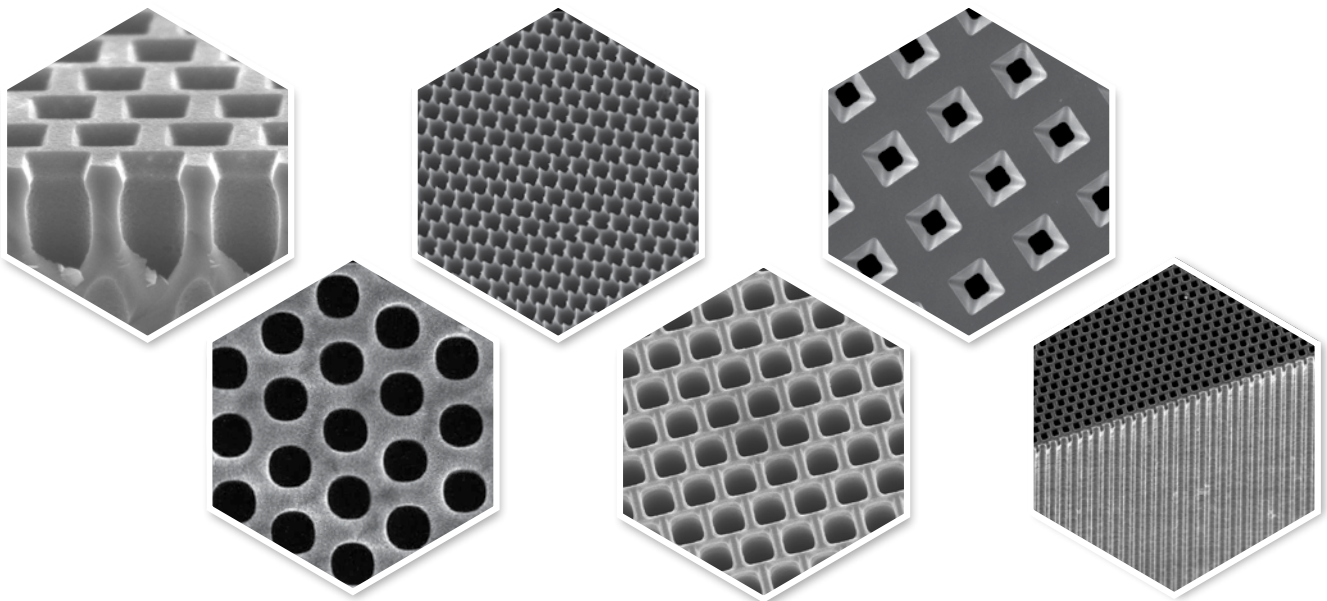
**Different optional post processing steps available:**  
Substrate liftoff for the generation of both side opened membranes, anisotropic pore shaping and laser dicing.



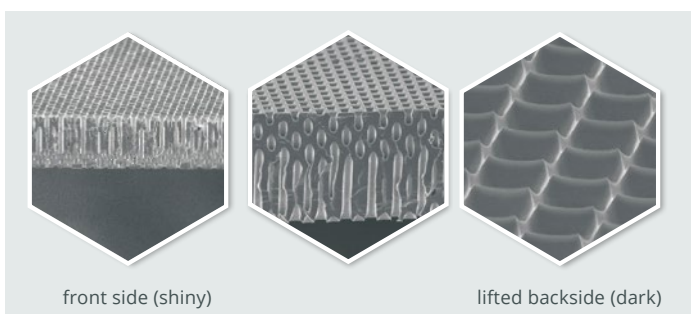
## STANDARD STOCK MATERIAL - DETAILS

Interpore distance	1.5 $\mu\text{m}$	4.2 $\mu\text{m}$	12 $\mu\text{m}$	20 $\mu\text{m}$
Pore diameter	1 $\mu\text{m}$	2.5 $\mu\text{m}$	5 $\mu\text{m}$	8 $\mu\text{m}$
Widening, upon request	up to 1.2 $\mu\text{m}$	up to 3.5 $\mu\text{m}$	up to 10 $\mu\text{m}$	up to 18 $\mu\text{m}$
Pore arrangement	trigonal	trigonal	square	square
Porosity	25 – 60 %	20 – 60 %	20 – 60 %	20 – 80 %
Membrane thickness, standard	50, 200 $\mu\text{m}$ (lift off)	50, 200 $\mu\text{m}$ (lift off)	50 (lift off), 350, 500 $\mu\text{m}$	350, 500 $\mu\text{m}$
Membrane thickness, upon request	25 – 200 $\mu\text{m}$	25 – 500 $\mu\text{m}$	25 – 500 $\mu\text{m}$	25 – 500 $\mu\text{m}$
Membrane size	up to 6" wafer with 130 mm porous area	up to 6" wafer with 130 mm porous area	up to 6" wafer with 130 mm porous area	up to 6" wafer with 130 mm porous area
Standard tolerances of $\pm 10\%$	✓	✓	✓	✓

## APPEARANCE AND DIMENSIONS



## THIN FREE STANDING MEMBRANES BY DIRECT LIFT OFF DURING PORE ETCHING



Lifted backsides are sensitive to mechanical handling. Abrasion of silicon nanotips is visible as brown "scratches" on the surface but have minor effect on the underlying pores.

**We suggest using vacuum tweezers on the front side or polymer tweezers on the side faces.**